

JULIET Series 3

Dynamic mixed-signal test and ISP for prototyping and production

- detecting shorts and opens
- evaluating external interfaces
- programming the assembly
- testing of application functionality
- calibrating the product



Technical specifications

Functional test

- discharge & power up
- drive & measure analogue voltages
- drive & measure digital sequences
- waveform generator & frequency measurement
- functional access to micro processors

Flash programming

- MCU internal Flash
- external flash (NAND, NOR)
- Bad Block Handling
- PLD/CPLD
- serial numbers
- production data
- calibration data

Software

- Tester Link Software (TLS)
- online PASS/FAIL statistics
- SYSTEM CASCON™
- optional MES interface

Tester interface

- 4 power supplies (internal/external)
- 4 test access ports (TAPs) at up to 100 MHz
- 192 hybrid I/O channels (A/D)
- 32 multi purpose ports (MPP)
- 16 differential channels
- 8/8 electrical isolated I/O channels
- 32 analogue measuring channels
- 16 analogue output channels
- 8 interfaces for RS232
- 4 interfaces for RS485

Mechanics

- compact, portable desktop device
- industrial mechanics for manual loading and unloading
- changeable bed of nail fixtures
- LED display of all test results
- operation via control panel during production
- front interface for convenient signal measuring

Embedded JTAG

- IEEE 1149.1 - Boundary Scan
- IEEE 1149.6 - differential Boundary Scan
- IEEE 1149.8.1 - selective toggle
- interconnection/RAM/cluster test
- JTAG, SWD, BDM, SBW, PIC1x
- VarioTAP®, ChipVORX®, VarioCore

PCB requirements

- max. board dimensions: 340 mm x 230 mm
- both side contacting down to 50 mil
- max. count of nails: 600 (1 N)

Environment

- electrical connection: 100-240 VAC, 50-60 Hz, max. 350 W
- dimensions: 630 mm (W) x 550 mm (D) x 345 mm (H)
- weight: approx. 30 kg
- environment temperature: 10-35 °C

